
SK6812RGBXP8-XX

SPECIFICATION

INTEGRATED LIGHT SOURCE INTELLIGENT CONTROL OF CHIP-ON-TOP SMD TYPE LED

Document No.: SPC/ SK6812RGBXP8-XX

Model No.: SK6812RGBXP8-XX

Description: 5.5x5.0x1.6mm Top SMD Type 0.2 Watt Power
Integrated light source Intelligent control LED

Rev. No.: 02

Date: 2015-08-20

正式规格书



INTEGRATED LIGHT SOURCE INTELLIGENT CONTROL
OF CHIP-ON-TOP SMD TYPE LED

Model: SK6812RGBXP8-XX

1. Product Overview :

SK6812RGBXP8 is a smart LED control circuit and light emitting circuit in one controlled LED source, which has the shape of a 5050 LED chip. Each lighting element is a pixel, and the intensities of the pixels are contained within the intelligent digital interface input. The output is driven by patented PWM technology, which effectively guarantees high consistency of the color of the pixels. The control circuit consists of a signal shaping amplification circuit, a built-in constant current circuit, and a high precision RC oscillator.

The data protocol being used is unipolar NRZ communication mode. The 32-bit data is transmitted from the controller to DIN of the first element, and if it is accepted it is extracted pixel to pixel. After an internal data latch, the remaining data is passed through the internal amplification circuit and sent out on the DO port to the remaining pixels. The pixel is reset after the end of DIN. Using automatic shaping forwarding technology makes the number of cascaded pixels without signal transmission only limited by signal transmission speed.

The LED has a low driving voltage (which allows for environmental protection and energy saving), high brightness, scattering angle, good consistency, low power, and long life. The control circuit is integrated in the LED above.

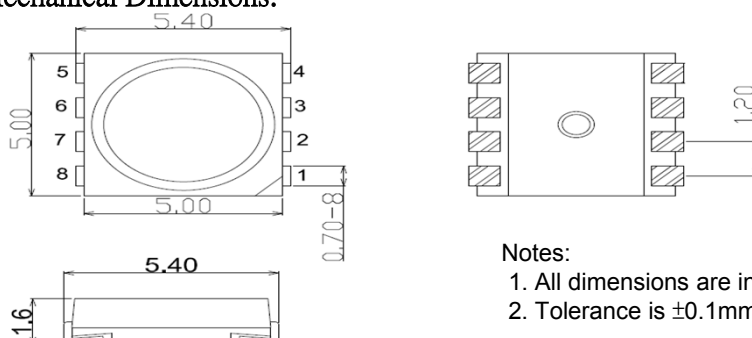
2. Main Application Field:

- Full color LED string light, LED full color module, LED super hard and soft lights, LED guardrail tube, LED appearance / scene lighting
- LED point light, LED pixel screen, LED shaped screen, a variety of electronic products, electrical equipment etc..

3. Description:

- Top SMD internal integrated high quality external control line serial cascade constant current IC;
- control circuit and the chip in SMD 5050 components, to form a complete control of pixel, color mixing uniformity and consistency;
- built-in data shaping circuit, a pixel signal is received after wave shaping and output waveform distortion will not guarantee a line;
- The built-in power on reset and reset circuit, the power does not work;
- gray level adjusting circuit (256 level gray scale adjustable);
- red drive special treatment, color balance;
- line data transmission;
- plastic forward strengthening technology, the transmission distance between two points over 10M;
- Using a typical data transmission frequency of 800 Kbps, when the refresh rate of 30 frames per sec

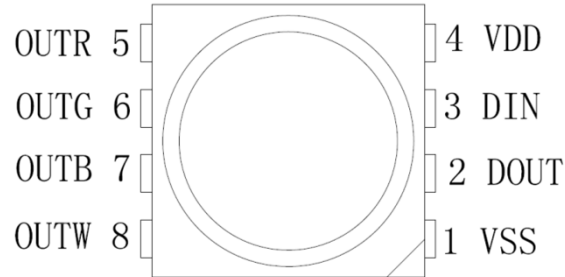
4. Mechanical Dimensions:



Notes:

1. All dimensions are in millimeters.
2. Tolerance is ± 0.1 mm unless otherwise noted

5. PIN configuration



The serial number	symbol	Pin name	Functional description
1	VSS	Ground wire	Signal grounding and power supply
2	DOUT	Data output	Control data signal output
3	DIN	Data input	Control signal input data
4	VDD	The power supply	Power supply pin
5	OUTR	Output port	Red output port
6	OUTG	Output port	Green output port
7	OUTB	Output port	Blue output port
8	OUTW	Output port	White output port

6. General Information

SK6812RGBXP8-XX

SK6812RGBWP8: Defaults to chip and IC integrated together, and in the form of SOP8 encapsulation structure

RGBX:

R 620-630NM
G 515-530NM
B 460-470NM

X: W White Color
A 585-595nm

XX: BW Blue White 5800-7500K
NW Natural White 3800-4500K
WS Warm Sunlight 2700-3200K

7. Absolute Maximum Ratings (Ta=25°C, VSS=0V) :

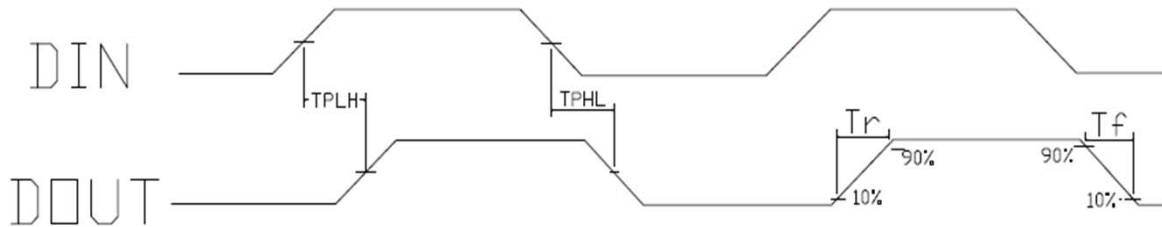
Parameter	Symbol	Range	Unit
Power supply voltage	VDD	+3.5~+5.5	V
Logic input voltage	V _{IN}	-0.5~VDD+0.5	V
Working temperature	T _{opt}	-40~+85	°C
Storage temperature	T _{stg}	-50~+150	°C
ESD pressure	V _{ESD}	4K	V

8. The electrical parameters (unless otherwise specified, TA=-20 ~ +70 °C, VDD=4.5 ~ 5.5V, VSS=0V):

Parameter	Symbol	Min	Typical	Max	Unit	Test conditions
The chip supply voltage	VDD	---	5.2	---	V	---
R/G/B port pressure	VDS,MAX	---	---	26	V	---
DOUT drive capability	IDOH	---	49	---	mA	DOUT connect ground, the maximum drive current
	IDOL	---	-50	---	mA	DOUT connect +, the largest current
The signal input flip threshold	VIH	3.4	---	---	V	VDD=5.0V
	VIL	---	---	1.6	V	
The frequency of PWM	FPWM	---	1.2	---	KHZ	---
Static power consumption	IDD	---	1	---	mA	---

9. The dynamic parameters (Ta=25 °C):

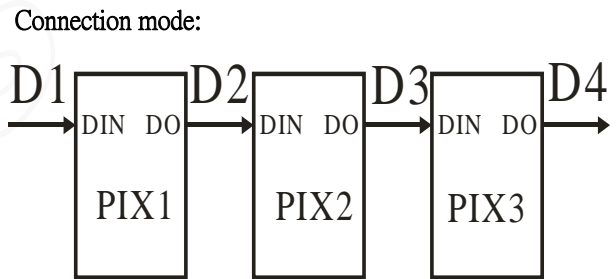
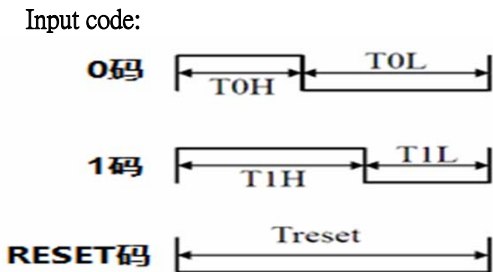
Parameter	Symbol	Min	Typical	Max	Unit	Test conditions
The speed of data transmission	fDIN	---	800	---	KHZ	The duty ratio of 67% (data 1)
DOUT transmission delay	TPLH	---	---	500	ns	DIN→DOUT
	TPHL	---	---	500	ns	
IOUT Rise/Drop Time	Tr	---	---	40	ns	VDS=1.5 IOUT=9mA
	Tf	---	---	80	ns	



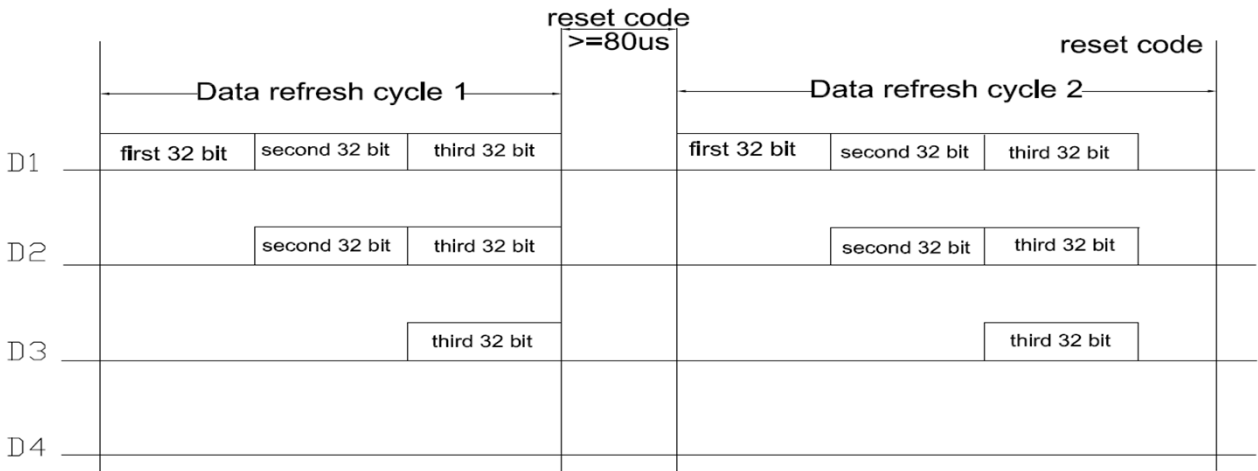
10. The data transmission time ($T_H+T_L=1.25\mu s\pm 600ns$):

T _{0H}	0 code, high level time	0.3 μs	$\pm 0.15\mu s$
T _{0L}	0 code, low level time	0.9 μs	$\pm 0.15\mu s$
T _{1H}	1 code, high level time	0.6 μs	$\pm 0.15\mu s$
T _{1L}	1 code, low level time	0.6 μs	$\pm 0.15\mu s$
Trst	Reset code, low level time	80 μs	

11. Timing waveform:



12. The method of data transmission:



Note: the D1 sends data for MCU, D2, D3, D4 for data forwarding automatic shaping cascade circuit.

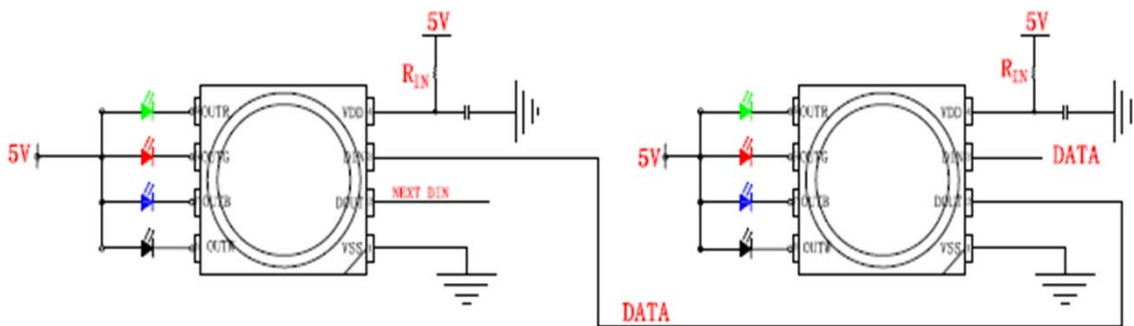
13. The data structure of 32bit:

R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4
G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
W7	W6	W5	W4	W3	W2	W1	W0				

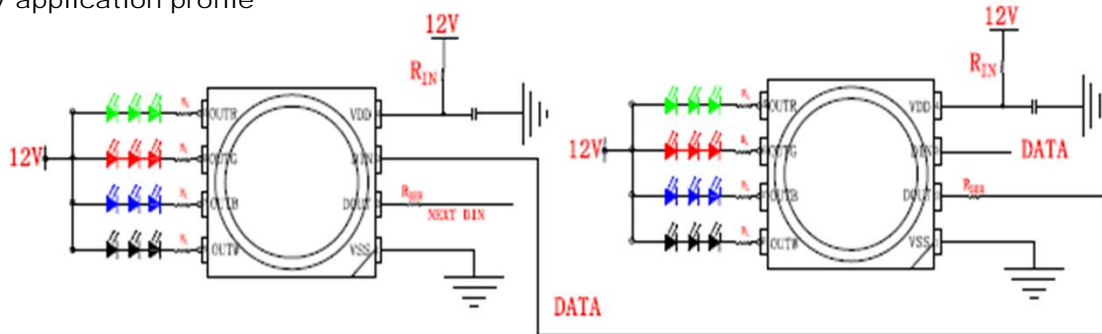
Note: high starting, in order to send data (R7 - R6 - W0)

14. The typical application circuit:

5V application profile

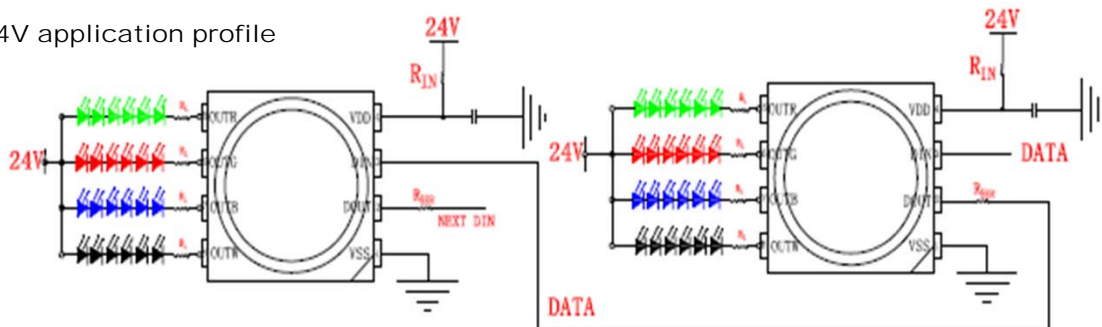


12V application profile



12 v application solutions, it is suggested that the concatenated signal output port 180 ohm resistor R_{SER} prevent electric plug or pull the power supply and signal lines reverse case damage IC output, concatenated 180 ohm resistor R_{SER} transmission distance between two points can be up to 10 m.

24V application profile



24 v application solutions, it is suggested that the concatenated signal output port 470 ohm resistor R_{SER} prevent electric plug or pull the power supply and signal lines reverse case damage IC output, concatenated 470 ohm resistor R_{SER} transmission distance between two points can be up to 5 m.

SK6812RGBWP8 typical application circuit parameter contains the power input voltage V_{IN} , current limiting resistor R_{IN} , chip VDD voltage capacitance C_{IN} and R/G/B LED current limiting resistor R_L .

Chip supply voltage VDD: $VDD = V_{IN} - (I_{DD} - I_{IN}) * R_{IN}$

I_{IN} is chip internal regulating circuit of working current, static current I_{DD} is chip (except for regulating circuit current), R_{IN} resistance must ensure that the $VDD > 4$ v.

R_{IN} the greater the resistance, the lower the power consumption of the system, but the system weak anti-interference ability; R_{IN} resistance is smaller, the greater the power consumption of the system, the working temperature is higher, the design should be based on the system application environment trade-offs resistance R_{IN} . V_{IN} and R_{IN} relationships shown in the table below:

V_{IN}	5V	6V	9V	12V	15V	18V	24V
R_{IN}	0	750R	3.6K	6.8K	10K	12K	18K

SK6812RGBWP8 data output port of the DOUT load equivalent capacitance C_L , every data transfer cycle DOUT must to C_L charging, the charging current transient about 40 ma. So the current limiting resistor R_{IN} the pressure drop of the instant increase, VDD voltage to drop, with stable voltage capacitor C_{IN} VDD voltage. C_L value does not exceed 1 nf cases, C_{IN} optional choose 0.1 uF capacitance.

The LED current limiting resistor R_L : $R_L = (V_{IN} - N * V_{LED} - V_{DS}) / I_{LED}$

The V_{IN} is the input voltage, V_{LED} is the pressure drop LED lights, V_{DS} is port voltage, reaches 1 v to constant output current, I_{LED} is port output current.

15. Hue Bin Specification for Yellow 、 Red 、 Green 、 Blue

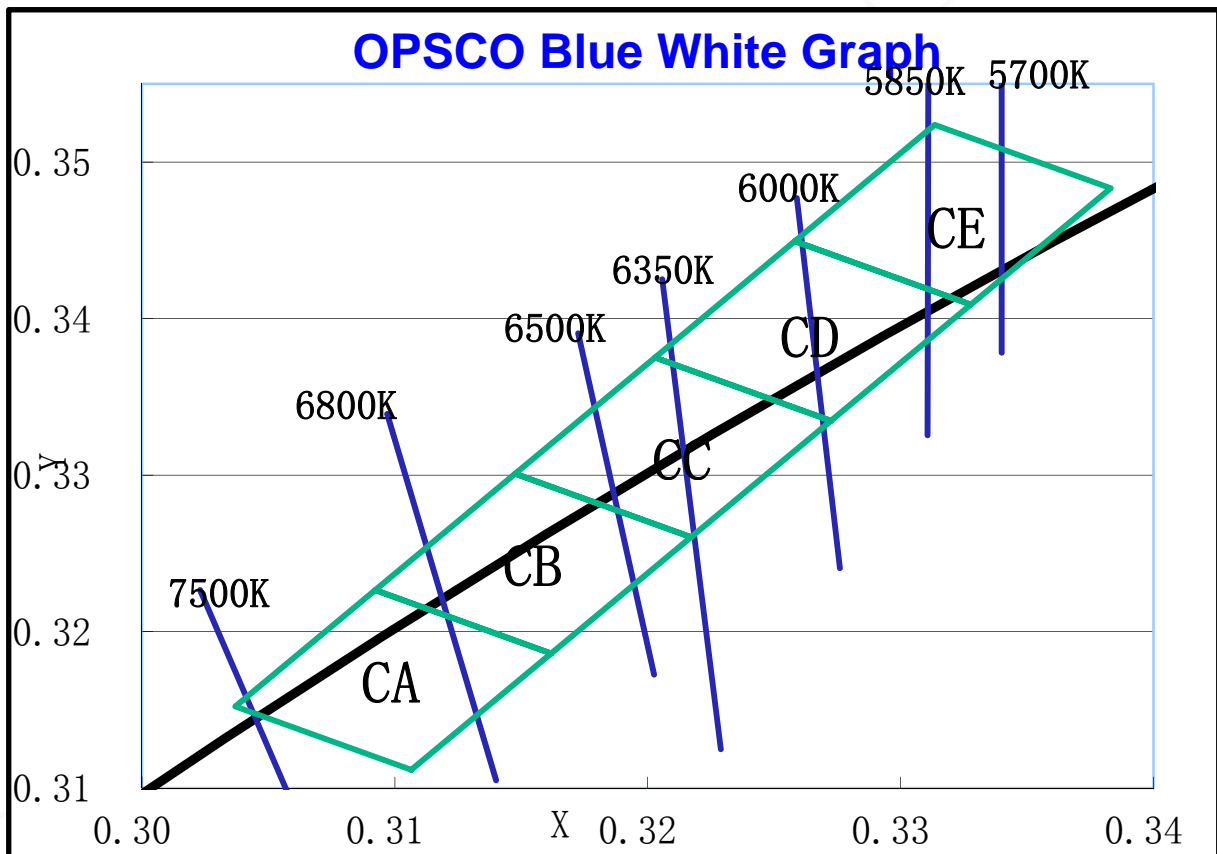
Name	Code	λ_d MIN (nm)	λ_d MAX (nm)
YELLOW	YL1	585	590
	YL2	590	595
RED	HR1	620	625
	HR2	625	630
BLUE	BL5	460	465
	BL6	465	470
GREEN	PG2	515	520
	PG3	520	525
	PG4	525	530

16. White Color Temperature Ranks & CIE Color Rank (Refer to CIE 1931 chromaticity diagram)

CIE chromaticity coordinates (ANSI Cool White)

CA	X1	Y1	X2	Y2	X3	Y3	X4	Y4
CA	0.305649	0.30617	0.29869	0.31022	0.30422	0.31765	0.31118	0.3136
CB	0.311181	0.3136	0.30422	0.31765	0.30975	0.32508	0.31671	0.32103
CC	0.316713	0.32103	0.30975	0.32508	0.31529	0.33252	0.32225	0.32847
CD	0.322245	0.32847	0.31529	0.33252	0.32082	0.33995	0.32778	0.3359
CE	0.327777	0.3359	0.32082	0.33995	0.32635	0.34738	0.33331	0.34333

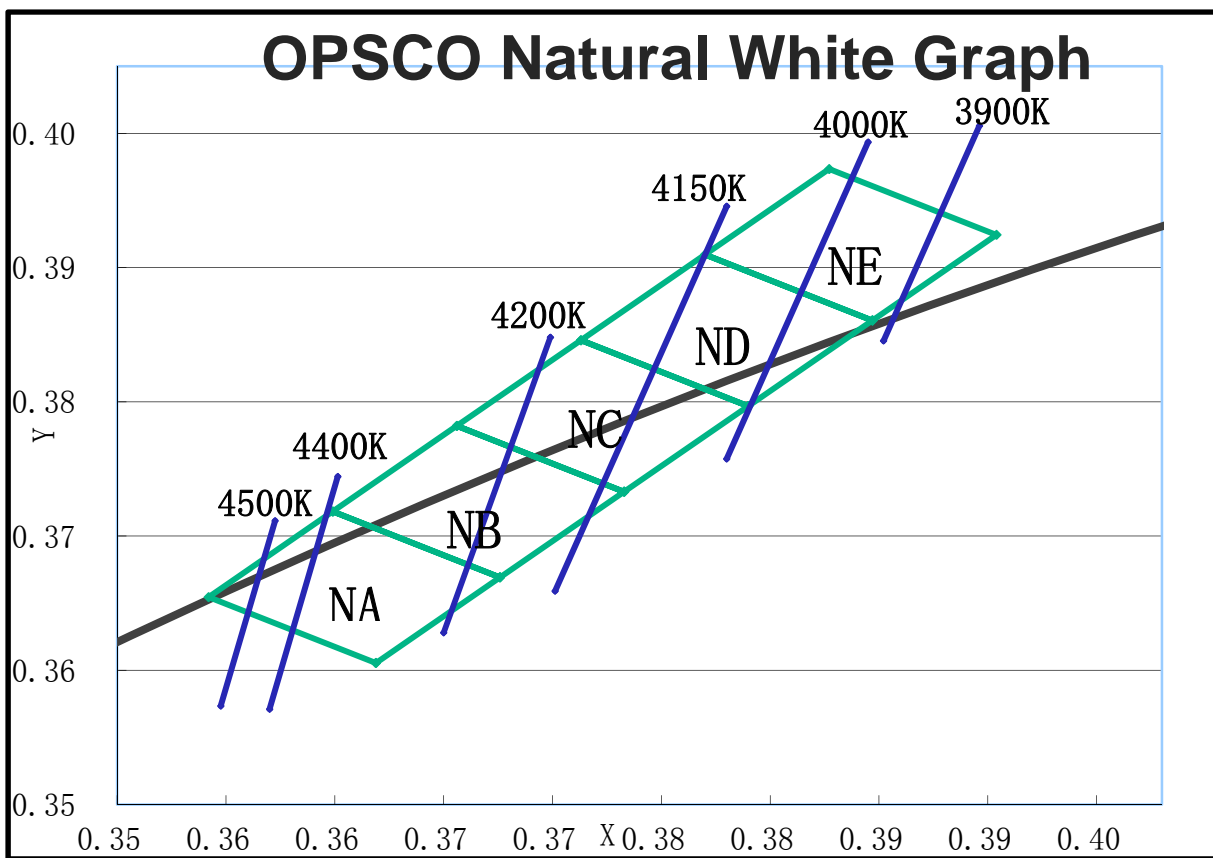
ANSI Blue White Color bin structures



CIE chromaticity coordinates (ANSI Natural white)

CA	X1	Y1	X2	Y2	X3	Y3	X4	Y4
NA	0.3662	0.3541	0.3578	0.3594	0.3623	0.3666	0.3707	0.3612
NB	0.3707	0.3612	0.3623	0.3666	0.3669	0.3737	0.3753	0.3683
NC	0.3753	0.3683	0.3669	0.3737	0.3714	0.3808	0.3798	0.3754
ND	0.3798	0.3754	0.3714	0.3808	0.3759	0.3879	0.3844	0.3825
NE	0.3844	0.3825	0.3759	0.3879	0.3805	0.3950	0.3889	0.3897

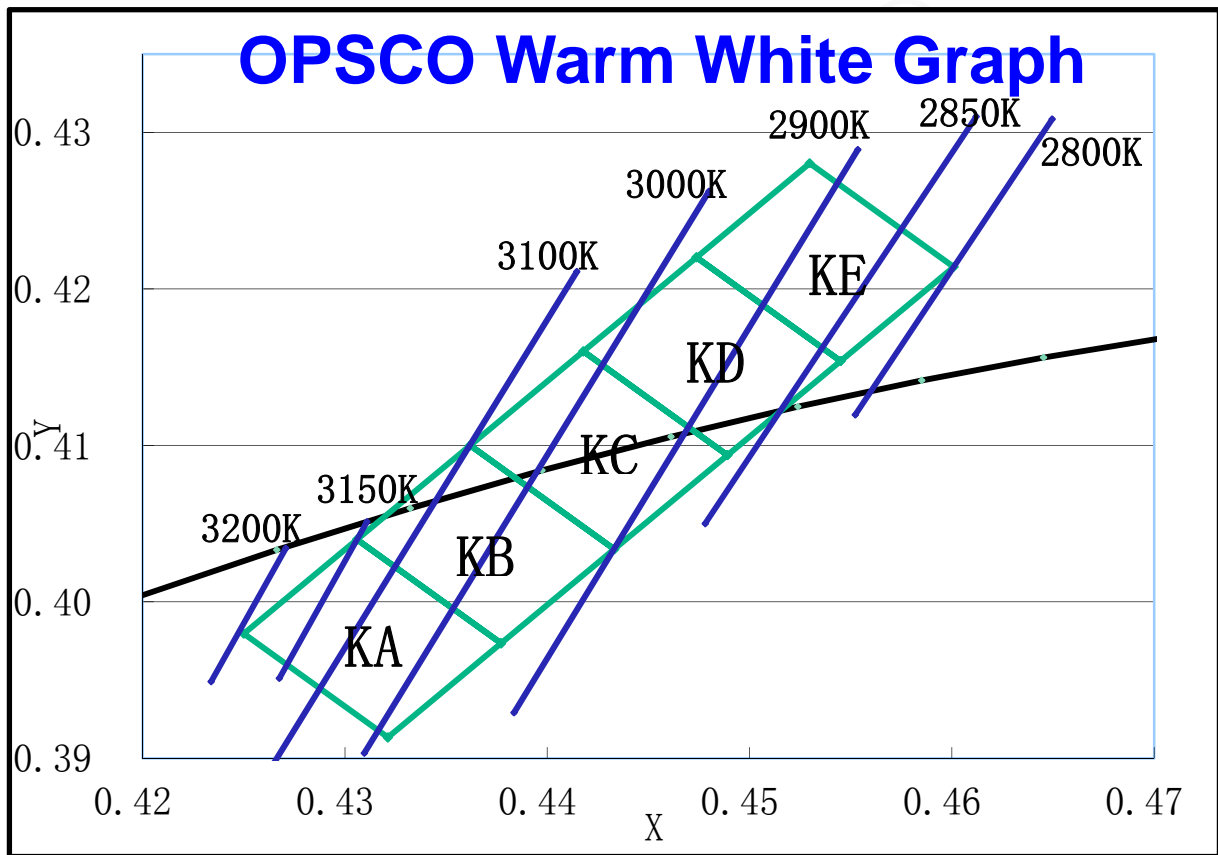
ANSI Natural White Color bin structures



CIE chromaticity coordinates (ANSI Warm White)

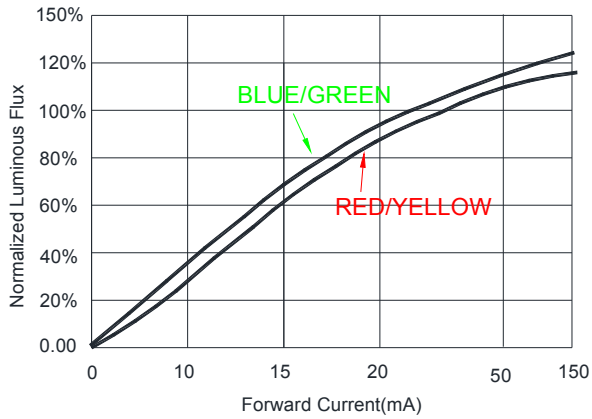
CA	X1	Y1	X2	Y2	X3	Y3	X4	Y4
KA	0.427	0.386	0.420	0.393	0.426	0.399	0.433	0.392
KB	0.433	0.392	0.426	0.399	0.431	0.405	0.438	0.398
KC	0.438	0.398	0.431	0.405	0.437	0.411	0.444	0.404
KD	0.444	0.404	0.437	0.411	0.442	0.417	0.449	0.410
KE	0.449	0.410	0.442	0.417	0.448	0.423	0.455	0.416

ANSI Warm White Color bin structures

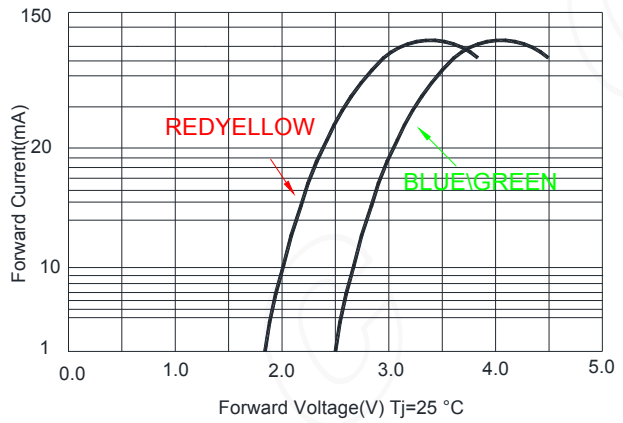


17. Standard LED Performance Graph:

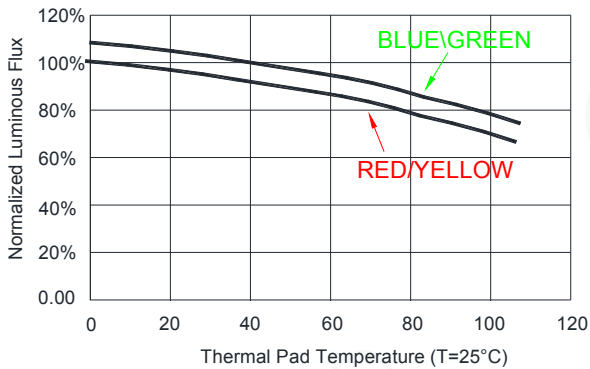
Typical Relative Luminous Flux vs. Forward Current



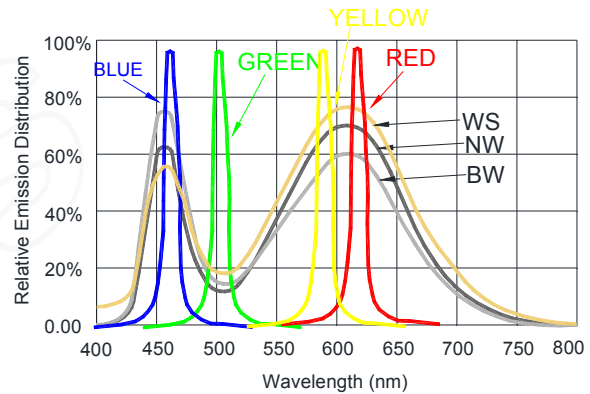
Forward Voltage vs. Forward Current



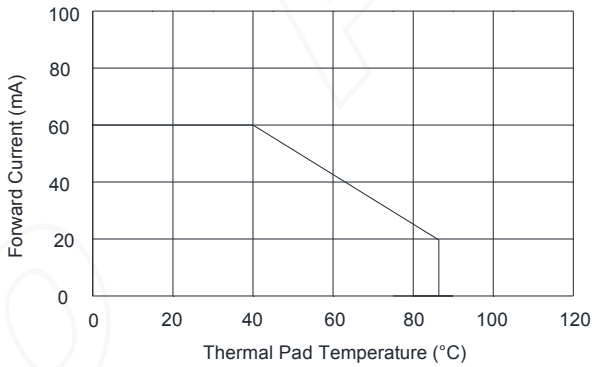
Thermal Pad Temperature vs. Relative Light Output



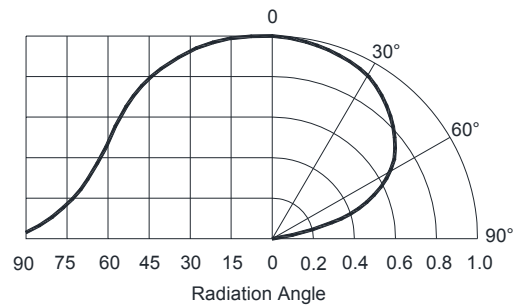
Wavelength Characteristics



Thermal Pad Temperature vs. Forward Current

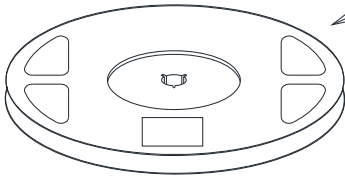
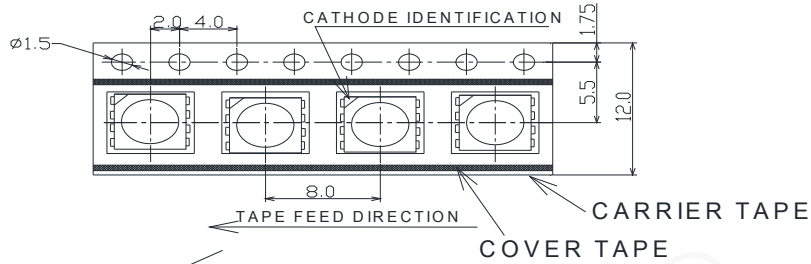


Typical Radiation Pattern 120°



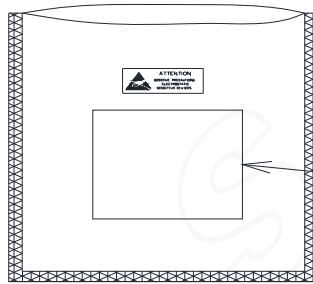
18. Packaging Standard:

SK6812RGBWP8-BW

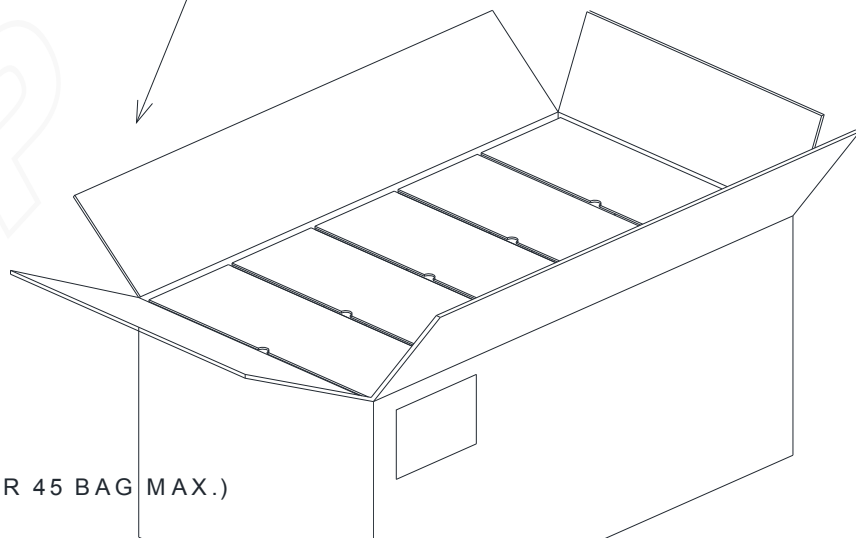


REEL (178x12mm)
(INNER 1000pcs LED MAX)

ESD POLYETHYLENE BAG



LABEL SKETCHING



CARDBOARD (INNER 45 BAG MAX.)

The reel pack is applied in SMD LED. The LEDs are packed in cardboard boxes after packaging in normal or anti-electrostatic bags. cardboard boxes will be used to protect the LEDs from mechanical shocks during transportation. The boxes are not water resistant and therefore must be kept away from water and moisture.

TOP SMD LED Application Notes

1. Features

The Purposes of making OPSCO's customers and users to have a clear understanding on the ways how to use the LED.

2. Description

Generally, The LED can be used the same way as other general purposed semiconductors. When using OPSCO's TOP SMD LED, the following precautions must be taken to protect the LED.

3. Cautions

3.1. Dust & Cleaning

This emitter has a silicone surface, There are many benefits to the silicone surface in terms of optical properties and improved reliability. However, silicone is a softer material and prone to attract dust. While a minimal amount of dust and debris on the LED will not cause significant reduction in illumination, steps should be taken to keep the emitter free of dust.

These include keeping the LEDs in the manufacturer's package prior to assembly and storing assemblies in an enclosed area after installing the emitters.

Surface condition of this device may change when organic solvents such as trichloroethylene or acetone were applied.

Avoid using organic solvent, it is recommended that isopropyl be used as a solvent for cleaning the LEDs. When using other solvents, it should be confirmed beforehand whether the solvents will dissolve the package and the resin or not.

Do not clean the LEDs by the ultrasonic. When it is absolutely necessary, the influence as ultrasonic cleaning on the LEDs depends on factors such as ultrasonic power. Baking time and assembled condition. Before cleaning, a pre-test should be done to confirm whether any damage to the LEDs will occur.

3.2. Moisture Proof Package

In order to avoid the absorption of moisture during transportation and storage, LED are packed in the aluminum envelop, A desiccant is included in the aluminum envelop as it absorbs moisture. When moisture is absorbed into the AMT package it may vaporize and expand during soldering. There is a possibility that this can cause exfoliation of the contacts and damage to the optical characteristics of the LEDs. For this reason, the moisture proof package is used to keep moisture to a minimum in the package.

3.3. Storage

In order to avoid the absorption of moisture, It is recommended to store SMD LED (in bulk or taped) in the dry box (or the desiccator) with a desiccant, Otherwise to store them in the following environment as recommended.

a. Temperature: 5°C~30°C

b. Humidity: 60% RH Max

It is recommended to solder the LED as soon as possible after unpacking the aluminum envelop, But in case that the LED have to be left unused after unpacking envelop again is requested.

The LED should be soldering within 1 hours after opening the package.

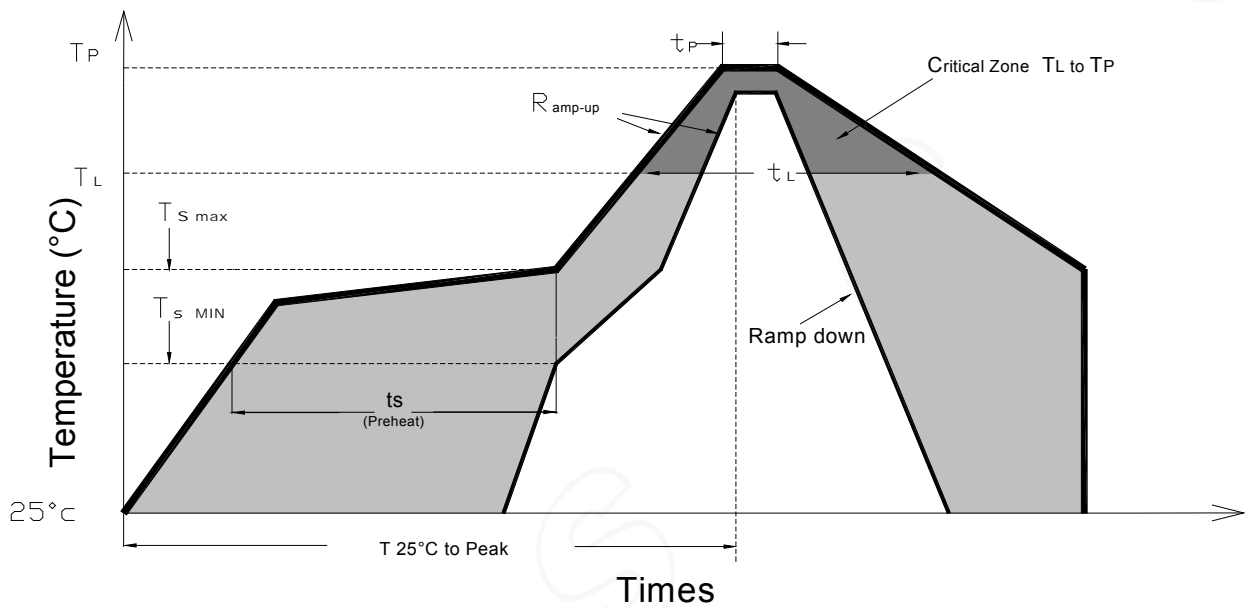
If baking is required, A baking treatment should be performed as follows:

70°C±5°C for more than 24 hours.

3.4. Reflow Soldering Characteristics

In testing, OPSCO has found S50 LEDs to be compatible with JEDEC J-STD-020C, using the parameters listed below. As a general guideline OPSCO recommends that users follow the recommended soldering profile provided by the manufacturer of solder paste used.

Note that this general guideline is offered as a starting point and may require adjustment for certain PCB designs and Configurations of reflow soldering equipment.



Profile Feature	Lead-Based Solder	Lead-Free Solder
Average Ramp-Up Rate ($T_{s \max}$ to T_p)	3°C/second max.	3°C/second max.
Preheat: Temperature Min ($T_{s \min}$)	100°C	150°C
Preheat: Temperature Min ($T_{s \max}$)	150°C	200°C
Preheat: Time ($t_{s \min}$ to $t_{s \max}$)	60-120 seconds	60-180 seconds
Time Maintained Above: Temperature (T_L)	183 °C	217 °C
Time Maintained Above: Time (t_L)	60-150 seconds	60-150 seconds
Peak/Classification Temperature (T_p)	215 °C	240 °C
Time Within 5°C of Actual Peak Temperature (t_p)	<10 seconds	<10 seconds
Ramp-Down Rate	6°C/second max.	6°C/second max.
Time 25 °C to Peak Temperature	<6 minutes max.	<6 minutes max.

Note: All temperatures refer to topside of the package, measured on the package body surface.

3.5 Heat Generation:

Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board, as well as components. It is necessary to avoid intense heat generation and operate within the maximum rating given in this specification. The operating current should be decided after considering the ambient maximum temperature of LEDs

3.6 Electrostatic Discharge & Surge Current :

Electrostatic discharge (ESD) or surge current (EOS) may damage LED.

Precautions such as ESD wrist strap, ESD shoe strap or antistatic gloves must be worn whenever handling of LED.

All devices, equipment and machinery must be properly grounded.

It is recommended to perform electrical test to screen out ESD failures at final inspection.

It is important to eliminate the possibility of surge current during circuitry design.

3.7 Moisture Proof Package

Cannot take any responsibility for any trouble that are caused by using the LEDs at conditions exceeding our specifications.

The LED light output is strong enough to injure human eyes. Precautions must be taken to prevent looking directly at the LEDs with unaided eyes for more than a few seconds.

The formal specification must be exchanged and signed by both parties before large volume purchase begins.

The appearance and specifications of the product may be modified for improvement without notice.

