#### Features:

- Built-in power clamp, support input voltage 5~24V
- OUT R/G/B/W constant curren default 17mA
- OUT R/G/B/W Power-on status: No light
- OUT R/G/B/W port withstand voltage 26V
- OUT R/G/B/W output gray level: 256 levels
- Data synchronization refresh in the same frame
- Data serial transmission by single |
- Unipolar return-to-zero code data protocol
- Built in signal reshaping circuit, to ensure waveform distortion do not accumulate after wave reshaping to the next driver
- Send data at speed of 800Kbps.
- SOP8 Package

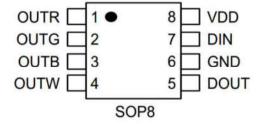
The SM16704 is 4 output channels control IC that special for LED driver circuit. Its internal includes power clamp module, signal decoding module Block, oscillation module, data regeneration module, output current drive module, etc.

IC use single ZR communication mode and adopt auto reshaping transmit technology, ensuring that the data is not fading during serial transmission.the DIN port receive data from controller, the first IC collect initial 32bit data then sent to the internal data latch, the other data which reshaping by the internal signal reshaping amplification circuit sent to the next cascade IC through the DO port.

#### Application field

- ·Building exterior / scene lighting
- Indoor&outdoor LED decorative lighting
- Pixel led lighting
- •Flexible led strip,linear led lighting

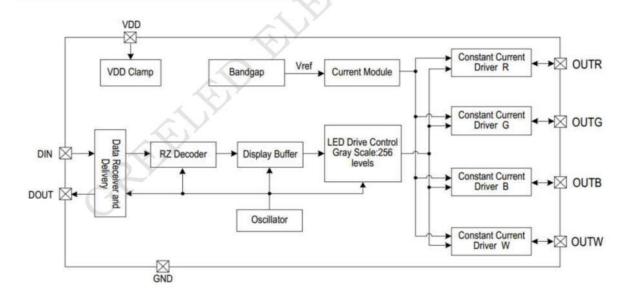
#### Footprint



#### **PIN Define**

NO.	Symbol	Function description
1	OUTR	Output of RED PWM control
2	OUTG	Output of GREEN PWM control
3	OUTB	Output of BLUE PWM control
4	OUTW	Output of White PWM control
5	DOUT	Data Output
6	GND	Data & Power Grounding
7	DIN	Control data input
8	VDD	IC power supply

# IC internal functional block diagram



## Absolute Maximum Ratings (TA=25℃, VSS=0V, unless otherwise noted.)

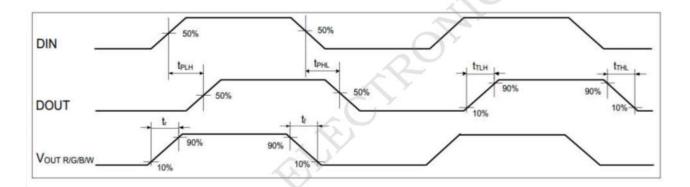
Symbol	Parameter	Ratings	Unit	
VDD	Power Supply Voltage	-0.4~5.5		
Vı	Logical Input Voltage	-0.4~VDD+0.4		
BVout	R/G/B Channel Output Port Withstand Voltage	30	V	
ICLAMP_MAX	Max clamp current	20	mA	
RθJA <sup>it 1</sup>	Thermal Resistance	130	.c./M	
Topt	Operation Temperature	-25~+85	rc	
Тѕтс	Storage Temperature Range	-55~150	°C	
Veso ESD		>2	KV	

## Electrical Characteristics (TA=-20~+70°C, VDD=4.8~5.5V, Vss=0V)

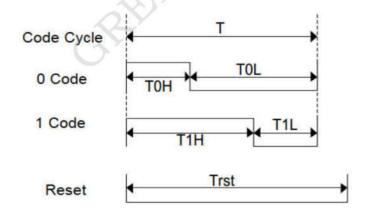
Symbol	Parameter	Test Conditions	Min	Tpy	Max	Unit
VDD	Internal Clamp Voltage	hetween VCC and VDD		5.2	5.5	v
	Power Voltage	VCC≤5V	3.0		5.0	٧
loo	Static Current	VDD = 4.5V, lour "OFF"	-	1.2		mA
VH	Input signal	DIN Input High Level	0.7xVDD		-	V
VL	theoshold voltage	DIN Input Low Level	-	-	0.3×VDD	V
Іон	Dout output current	DOUT output is high level, serially connect 10Ω resistor to GND	-	-40		mA
lou	Dout sink current	DOUT output is low, shorted to VDD	-	40	-	mA
Vos_s	OUT R/G/B/W Inflection point	Iour = 17mA	-	8.0	-	٧
%VS.Vps	Z-7	I <sub>OUT</sub> = 17mA, V <sub>DS</sub> = 1.0~3.0V		0.5		%
%VS.VDD	OUT R/G/B/W current Amout of change	lour =17mA, VDD = 4.5~5.5V	-	0.5	-	%
%VS.TA	James of Stimings	Iouτ = 17mA, T <sub>A</sub> = -40~+85℃		5.0	-	%
lleak	Leak current	Vos =26V, lour "OFF"	-	•	1	uA

## Switching characteristics(TA=-20~+70°C, VDD=4.8~5.5V, VSS=0V)

Symbol	Parameter	Conditions	Min	Тру	Max	Unit
fрим	OUT R/G/B/W output PWM frequency	IOUT=17mA, OUT port Serially connect 200Ω resistor to VDD		1.2		KHz
<b>t</b> PLH	Signal transmission dalay	DOUT port to ground load capacitance 30pF,		85		ns
<b>TPHL</b>	Signal transmission delay	Signal transmission delay from DIN to DOUT		70		ns
tтьн	DOUT conversion time	DOUT next to assured load assessitance 20nE	•	18		ns
tтнL	DOOT conversion time	DOUT port to ground load capacitance 30pF		20	<i>)</i> -	ns
tr	OUT R/G/B/W	JT R/G/B/W = 17mA, OUT R/G/B/W port serial connection		55		ns
tr	Conversion time	200Ω resistor to VDD, ground load capacitance 15pF	•	75		ns



## **Code Description**



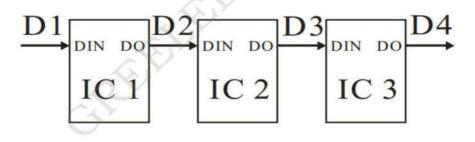
## **Data Transfer Time**

Symbol	Parameter	Min	Tpy	Max	Deviation	Unit
Т		1.2		*	±0.05	us
ТОН	T0H 0 code, high voltage time		0.3		±0.05	us
TOL	TOL 0 code, low voltage time		0.9		±0.05	us
T1H	T1H 1 code, high voltage time		0.9		±0.05	us
T1L 1 code, low voltage time			0.3	. 1	±0.05	us
Trst	Frame unit, low voltage time	200	-	. (- )		us

Composition of 32bit Data										
R7	R6	R5	R4	R3	R2	R1	R0	G7 ····· G0	В7 ВО	W7 W0

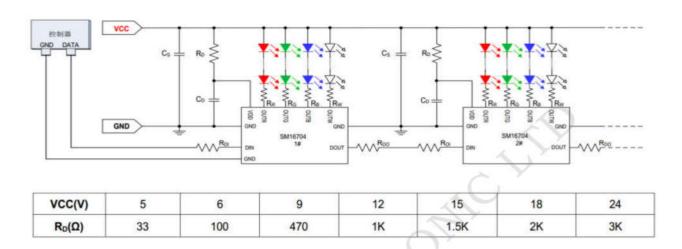
Note: Data transmit in order of RGB, high bit data at first.

### Cascade method

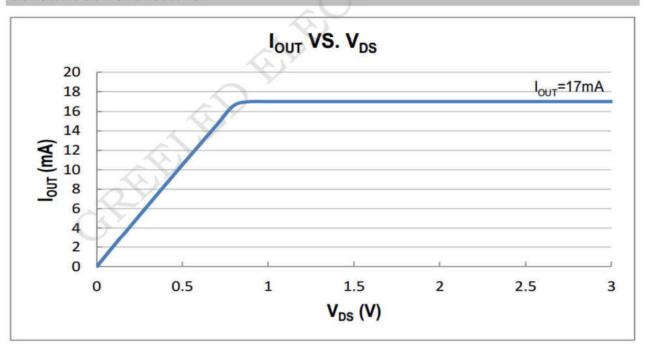


IC1	Trst	第一组32bits数据	第二组32bits数据	第三组32bits数据	Trst	
IC2	Trst		第二组32bits数据	第三组32bits数据	Trst	
IC3				第三组32bits数据	Trst	

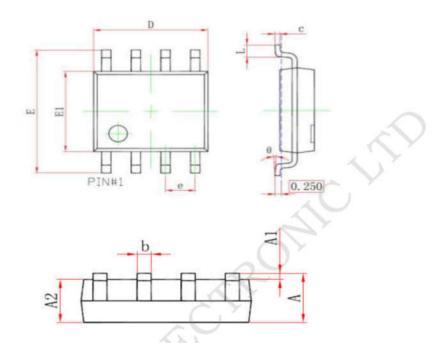
## **Typical Application Circuit**



#### **Constant Current Feature**



SOP8



Symbol	Min(mm)	Max(mm)
A	1.25	1.95
A1		0.25
A2	1.25	1.75
b	0.25	0.7
c	0.1	0.35
D	4.6	5.3
	1.27	BSC)
E	5.7	6.4
E1	3.7	4.2
L	0.2	1.5
θ	0°	10°

M/N	Package	Q	ΓY	Reel size
WITH	Package	PlasticTube	Reel	NCCI SIZC
SM16704	SOP8	100pcs/tube	4000pcs/reel	13"